Manish Prajapati

Bachelor of Technology in Electronics Engineering ,(+91) 9926975617 | 22ec3024@rgipt.ac.in | manishprajapati2756097@gmail.com

PROFILE

To obtain a challenging and rewarding position in a reputable organization where I can apply my skills and knowledge to contribute to the company's success.

Jais, Amethi, Uttar Pradesh

2022 - Present

Dharampuri, Dhar, MP

Dharampuri, Dhar, MP

Apr 2019 – Feb 2021

TECHNICAL SKILLS

Programming Languages: C, C++, Verilog HDL Software &

Tools: AutoCAD, Cadence Virtuoso

Electronics Skills: Analog Circuit Design, Digital Circuit Design

EDUCATION

Rajiv Gandhi Institute of Petroleum Technology

Bachelor of Technology, Electronics Engineering

CPI 9.30 (Department Rank 1)

Government Model Higher Secondary School

Intermediate (MPBSE; Madhya Pradesh Board of Secondary Education)

Percentage: 87.4%

Government Model Higher Secondary School

Matric (MPBSE) Feb 2019

Percentage: 87.8%

PROJECTS

DAC Circuit Design — Ongoing

Designing a Digital-to-Analog Converter (DAC) circuit as part of an ongoing academic project. This project involves creating a DAC that converts digital binary signals into precise analogue voltage levels, critical for interfacing digital systems with analogue devices. Focuses on optimization of resolution, speed, and power efficiency, using CADENCE for simulations and designs.